

RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITING PROCEDURE
EXAMINING GROUP 2815

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| Technology Center 2800 | (including cover sheet) |
| Facsimile Number: 571-273-8300 | Transmission Date: August 12, 2005 |

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lin, et al. Docket No.: TSM03-0670
Serial No: 10/729,092 Art Unit: 2815
Date Filed: December 5, 2003
Title: Structure and Method of Forming Integrated Circuits Utilizing Strained Channel Transistors

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- Certification of Facsimile Transmission (1 page)
- Amendment (14 pages)

Respectfully submitted,



Natalie Swider
Legal Assistant

Confirmation Respectfully Requested

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Examiner: Matthew E. Warren

For: Structure and Method of Forming Integrated Circuits Utilizing Strained
Channel Transistors

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

The following amendments and remarks are offered in response to Examiner's Office
Action dated June 14, 2005, a final rejection. Applicants respectfully request that this amendment
be entered pursuant to the provisions of 37 CFR § 1.116, and respectfully request reconsideration
of claims 1-18, 22-55 and 78-80.